

FIG.1

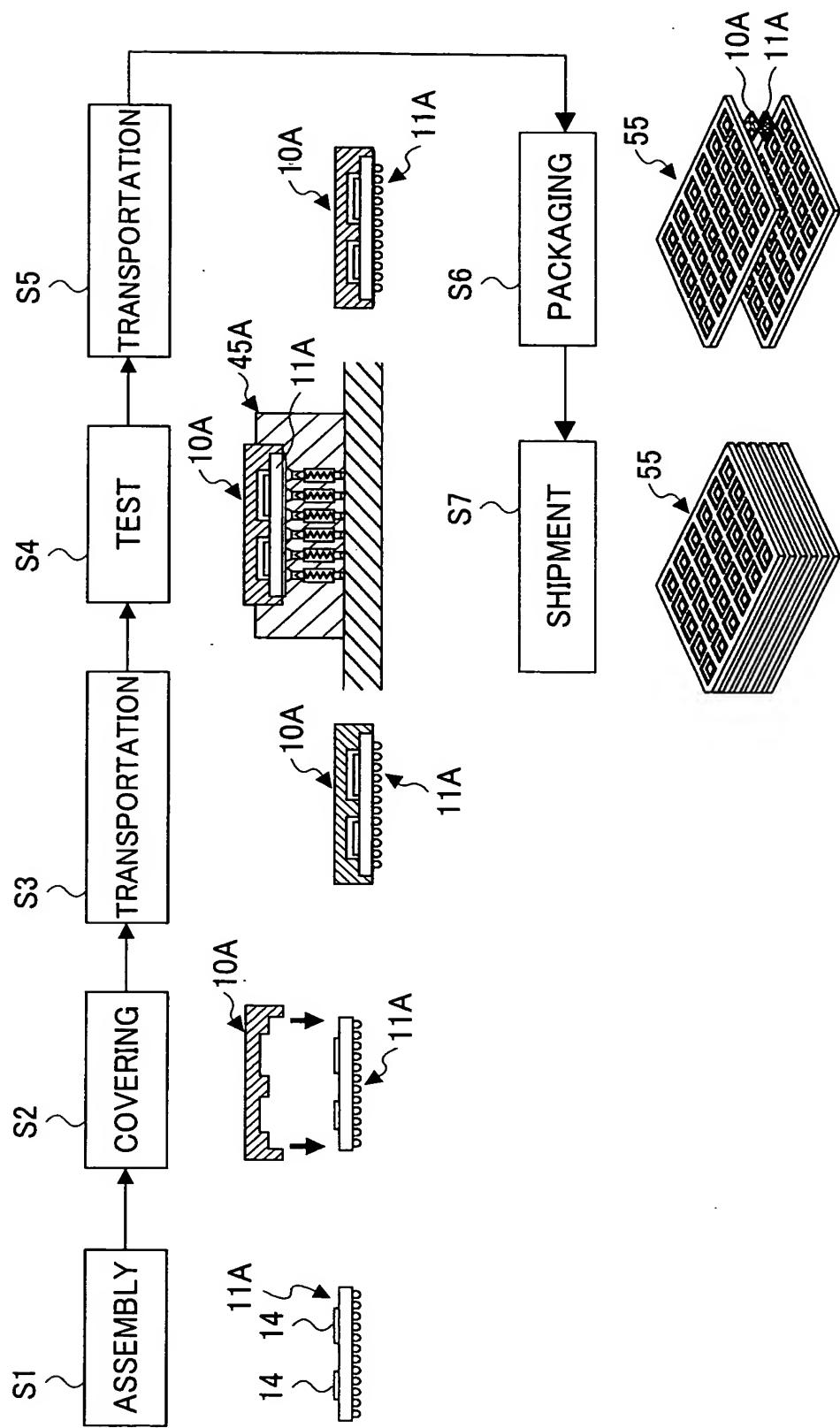


FIG.2A

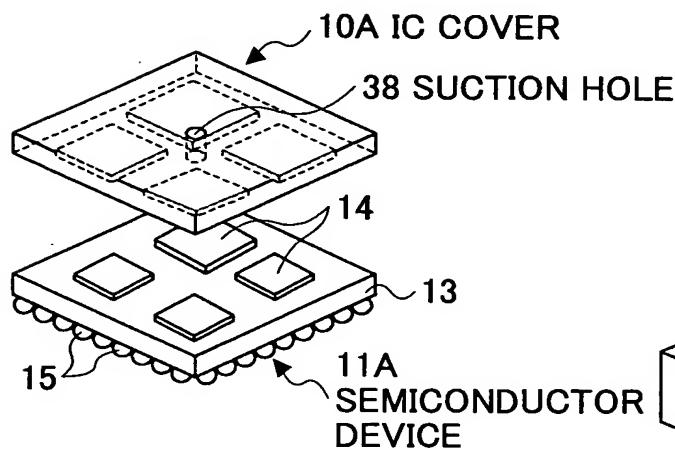


FIG.2D

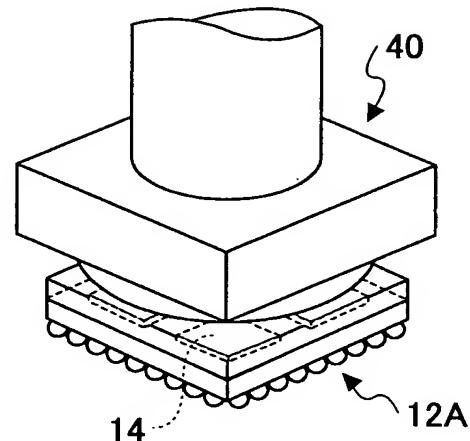


FIG.2B

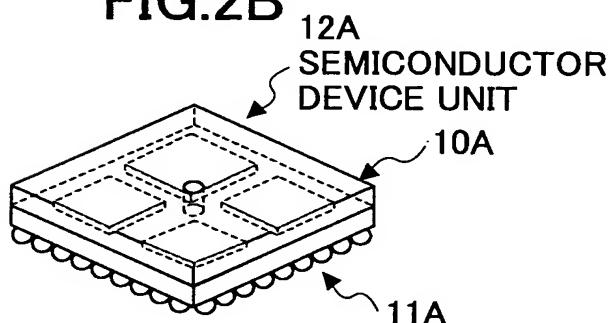


FIG.2E

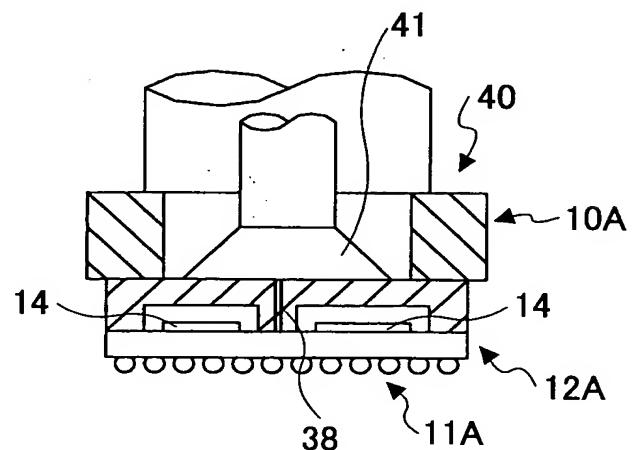


FIG.2C

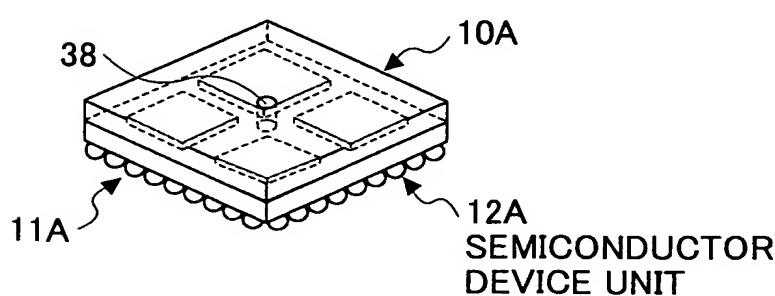
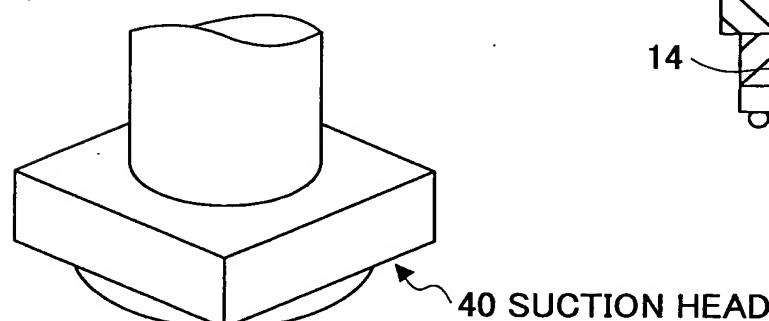


FIG.3

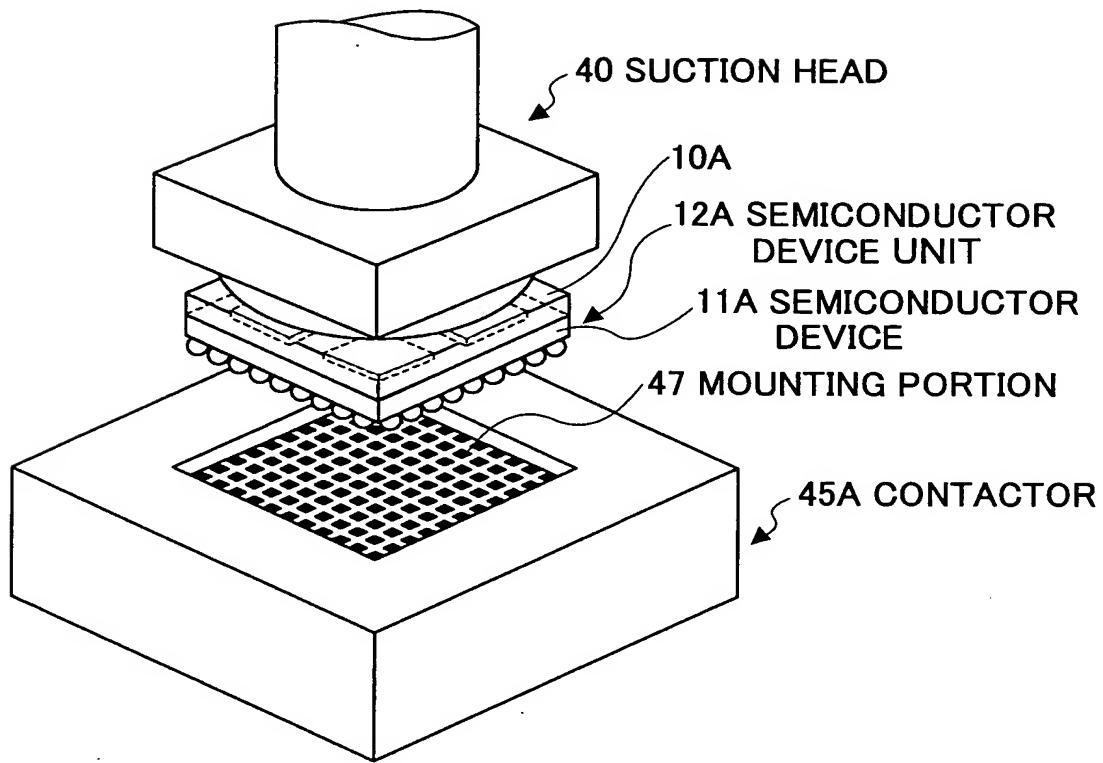


FIG.4

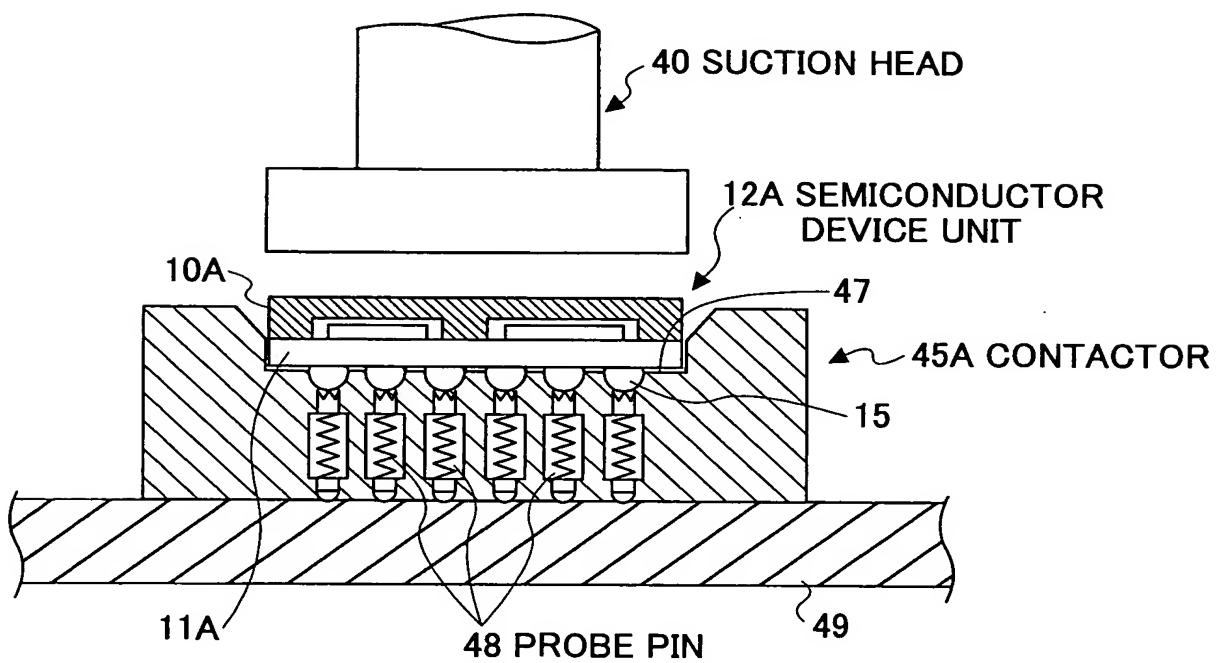


FIG.5A

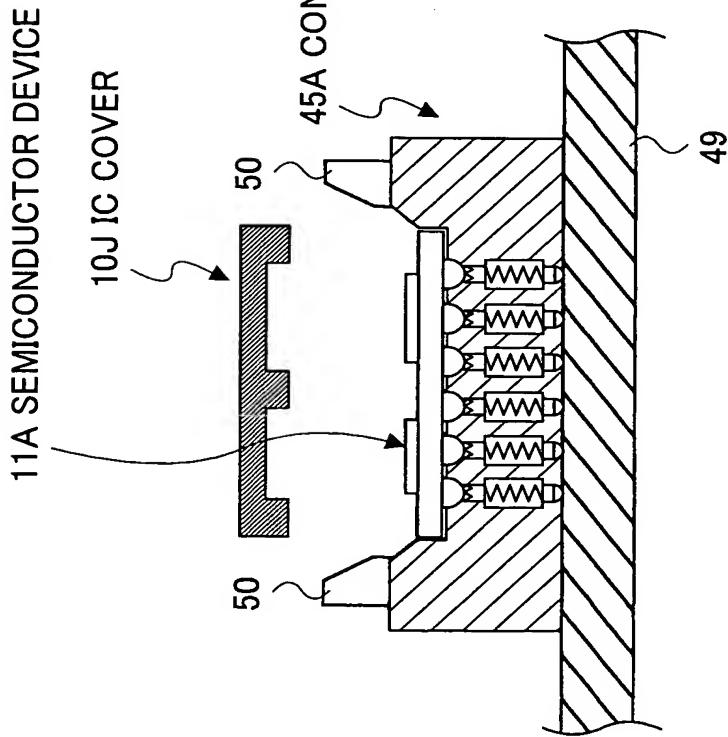


FIG.5B

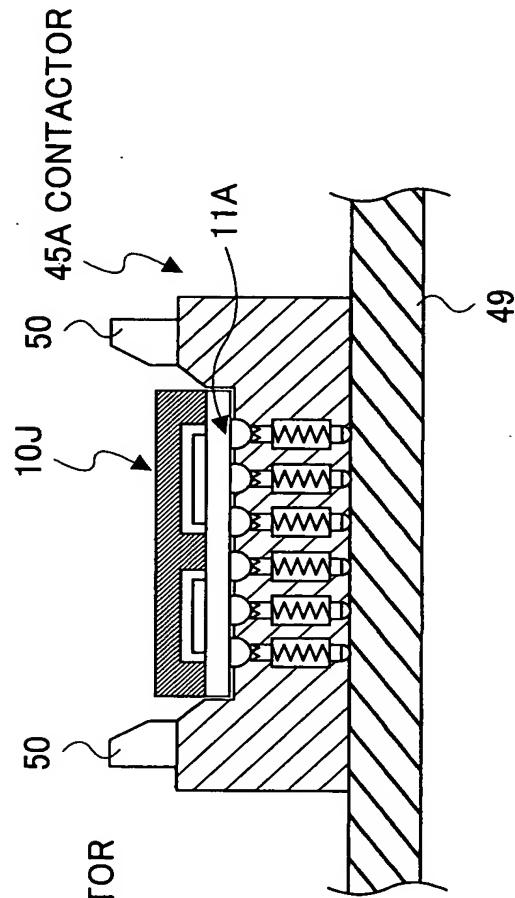


FIG.6

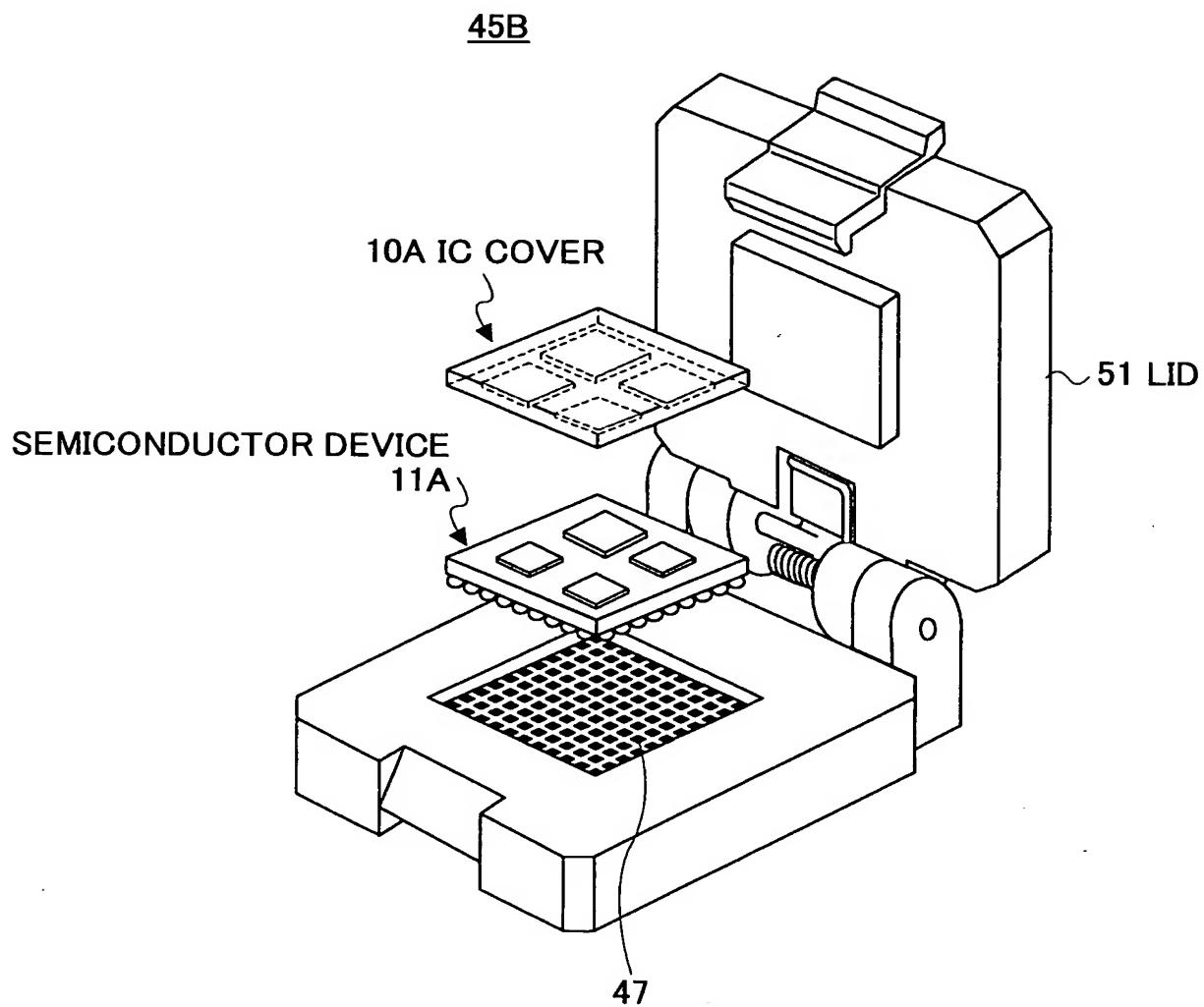


FIG. 7

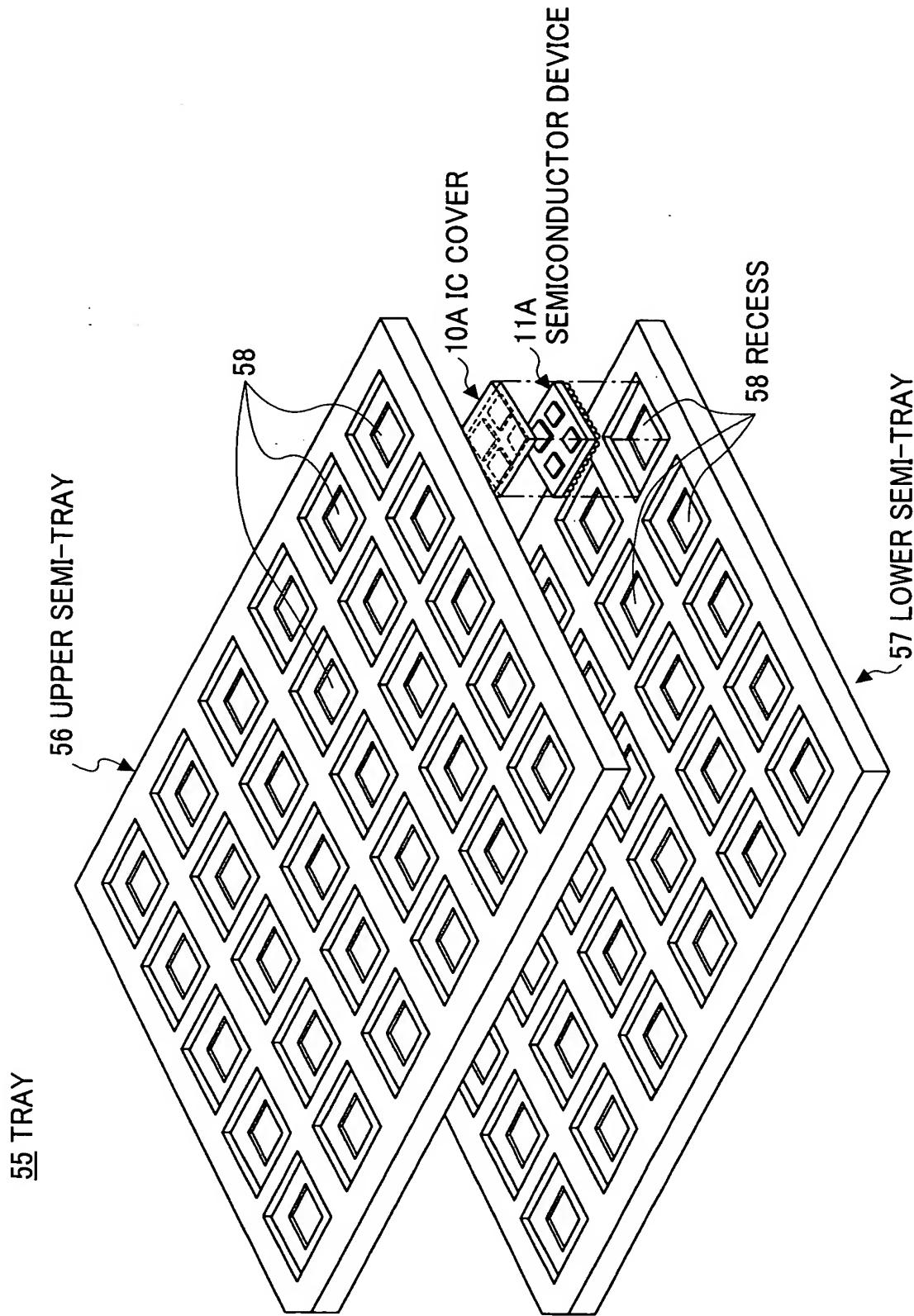


FIG.8

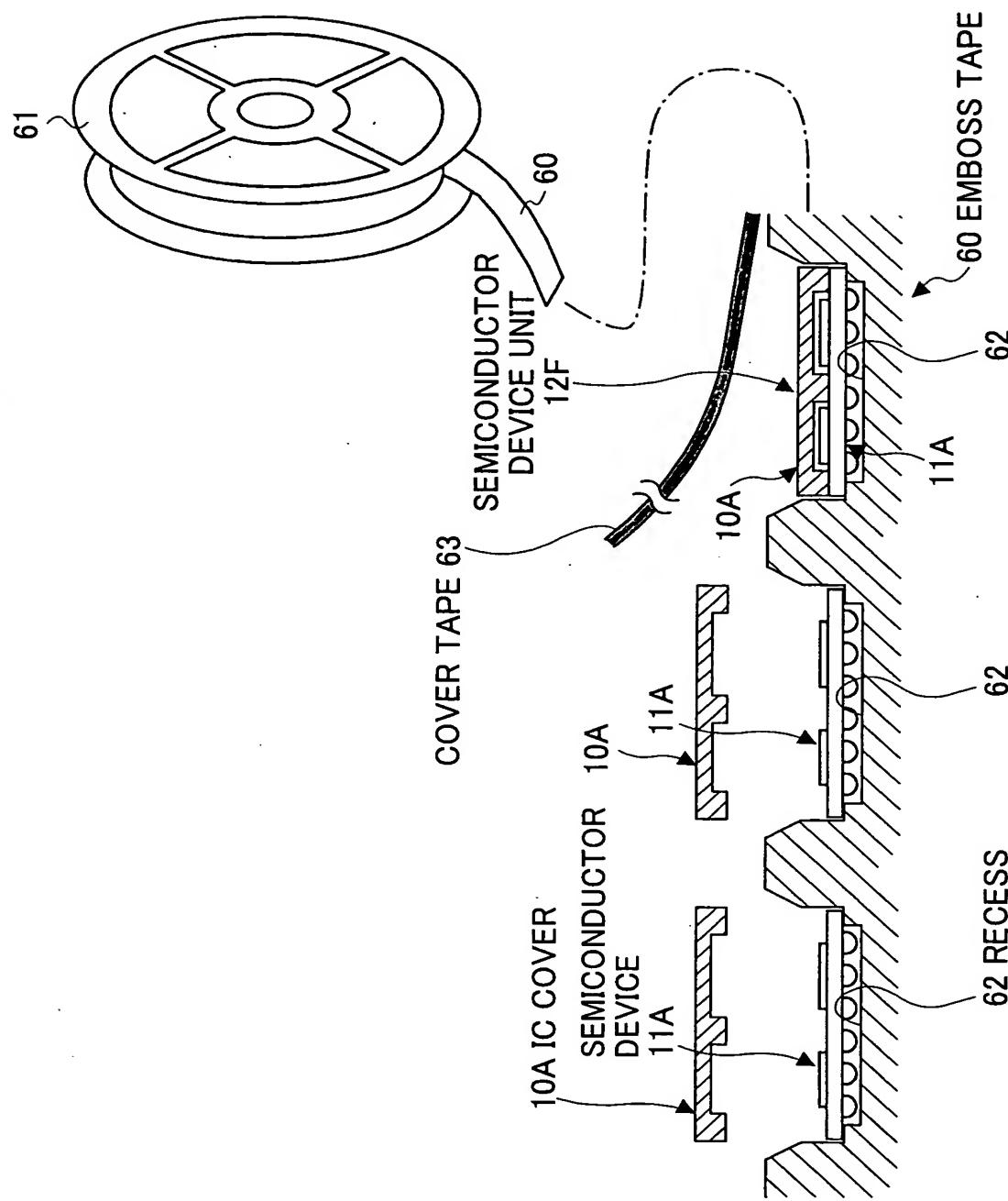


FIG. 9A
FIG. 9B

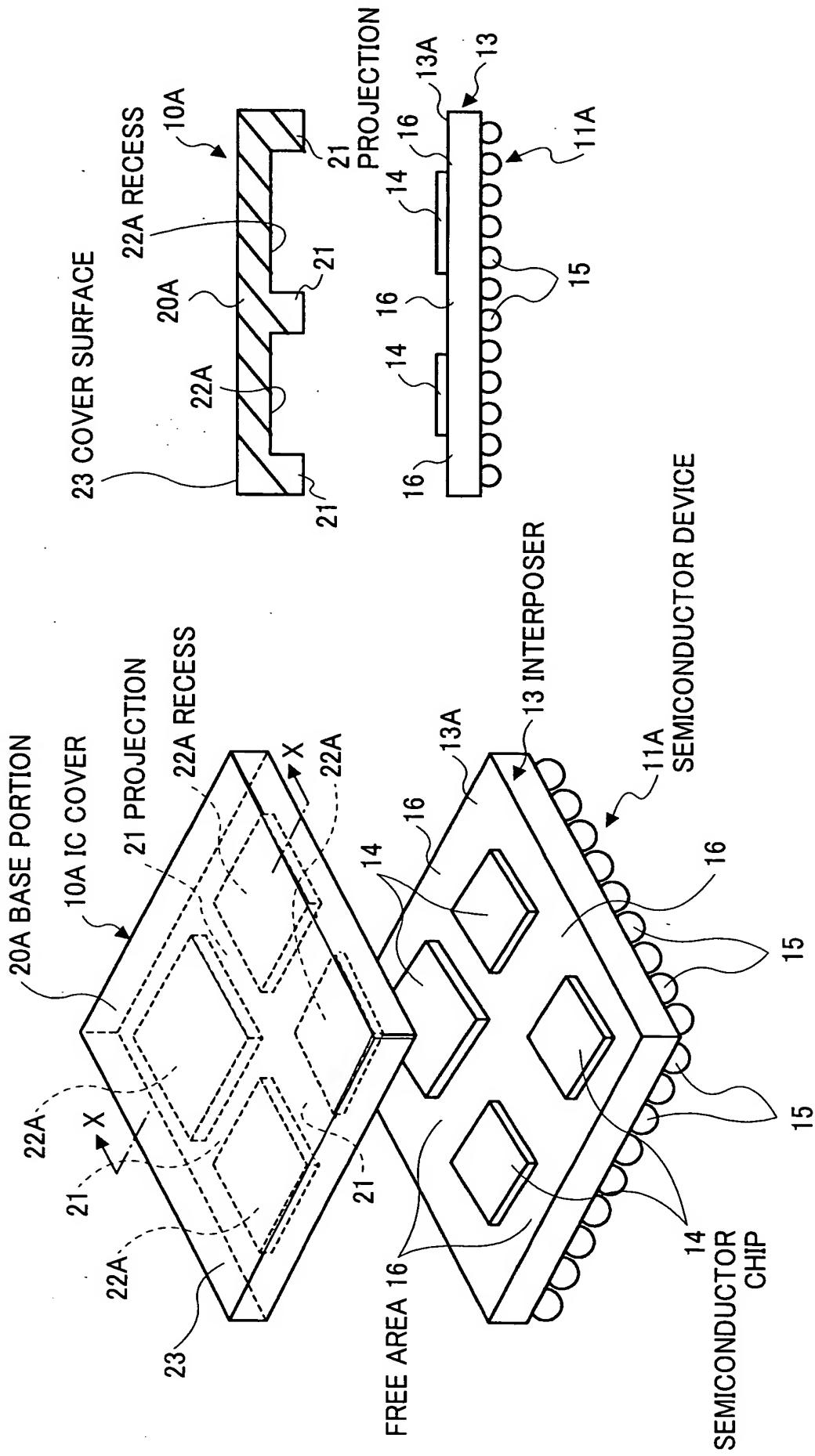
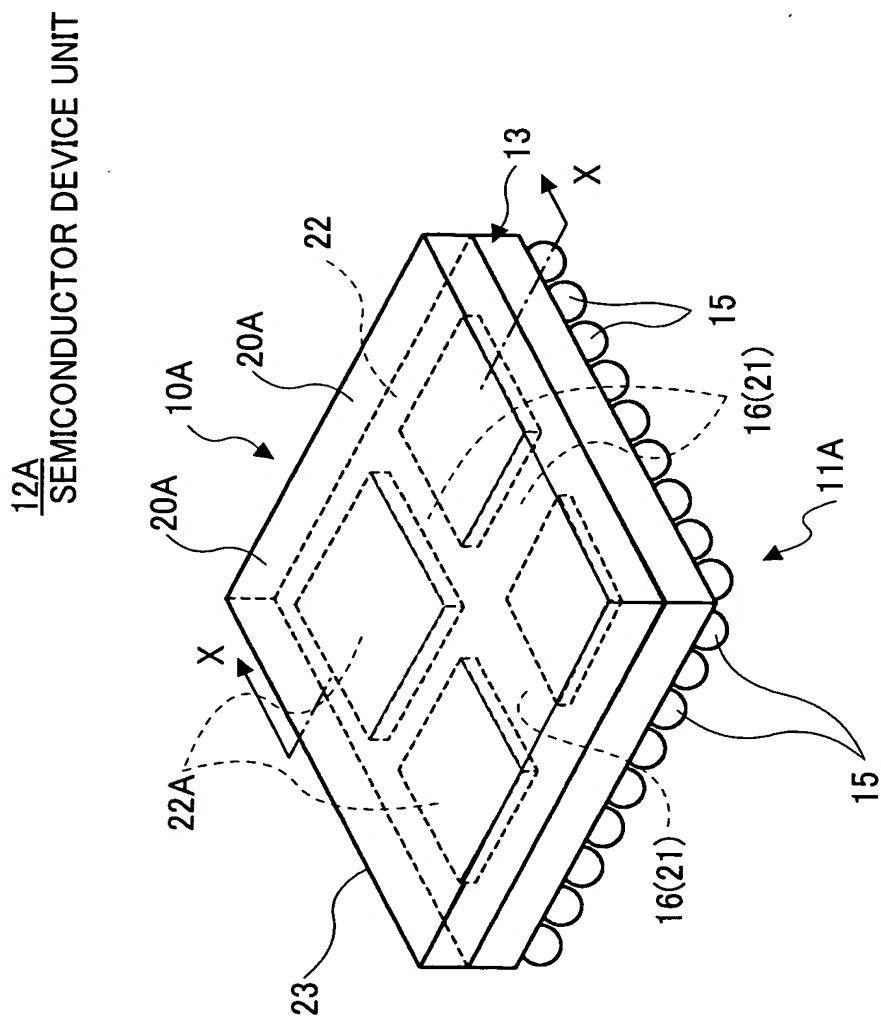


FIG. 10A



12A

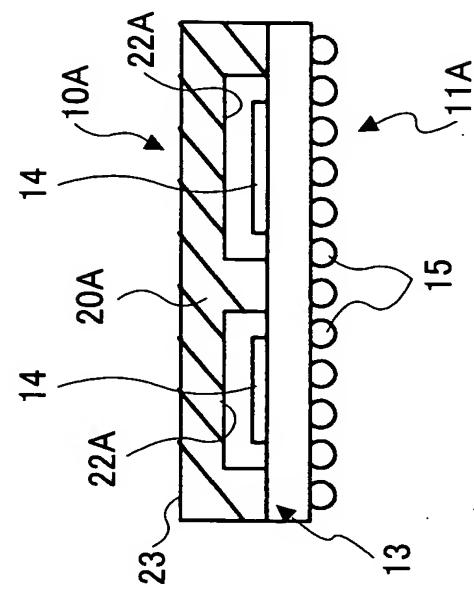


FIG.11

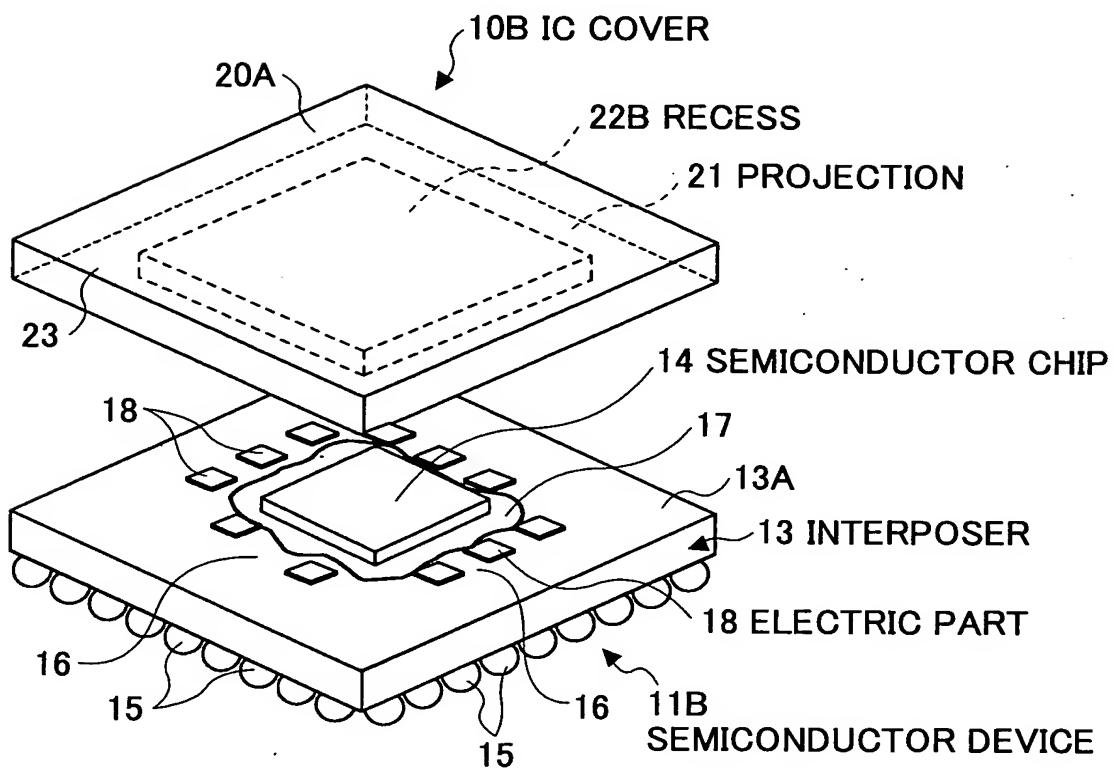


FIG.12

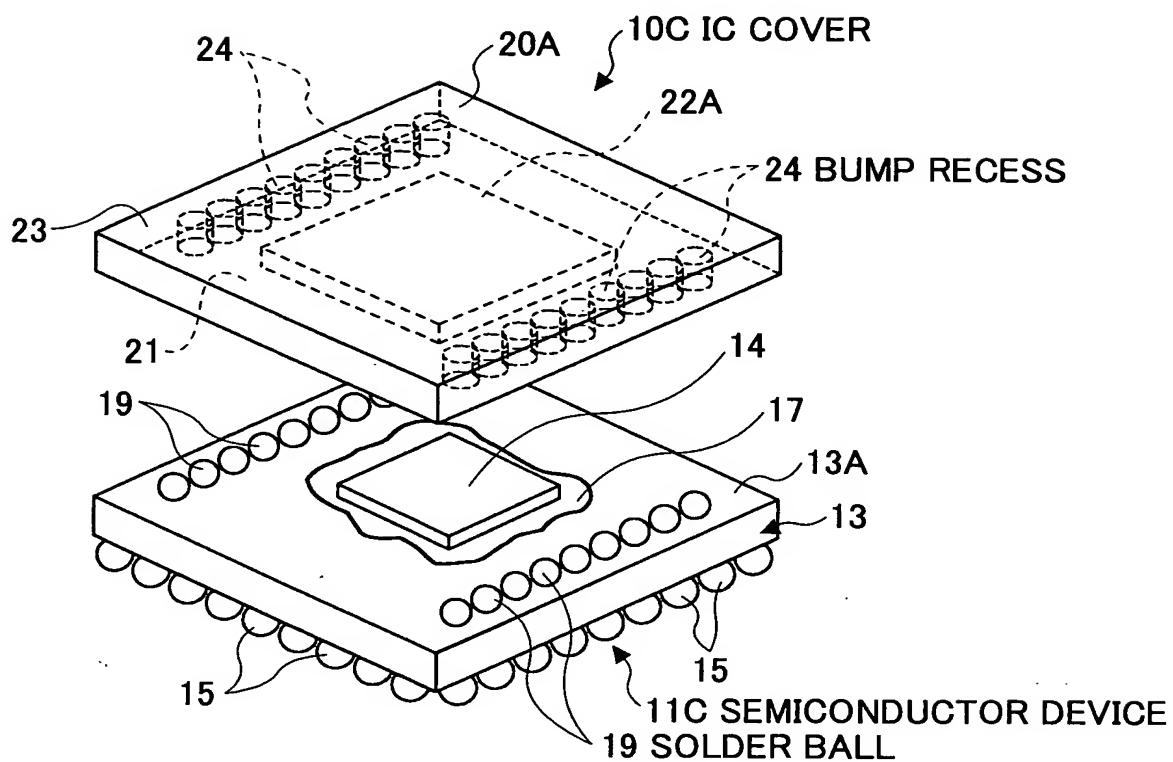


FIG.13A

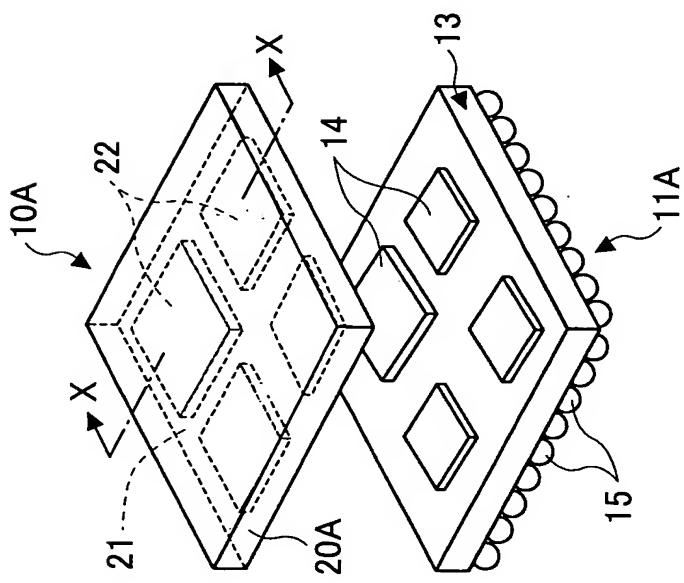


FIG.13C

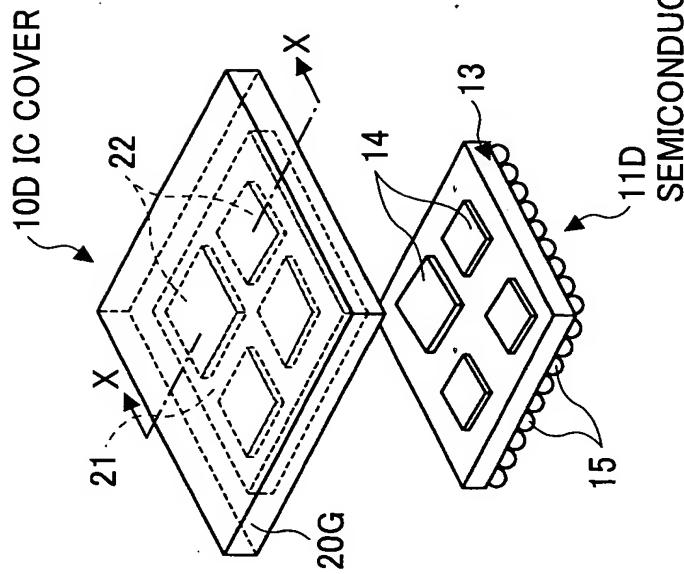


FIG.13B

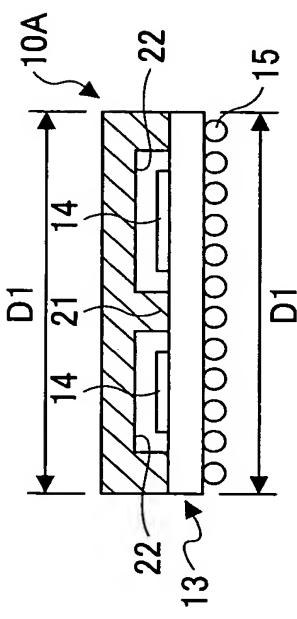
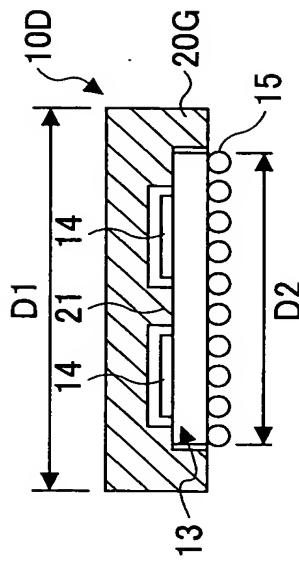


FIG.13D



11D SEMICONDUCTOR DEVICE

FIG. 14A

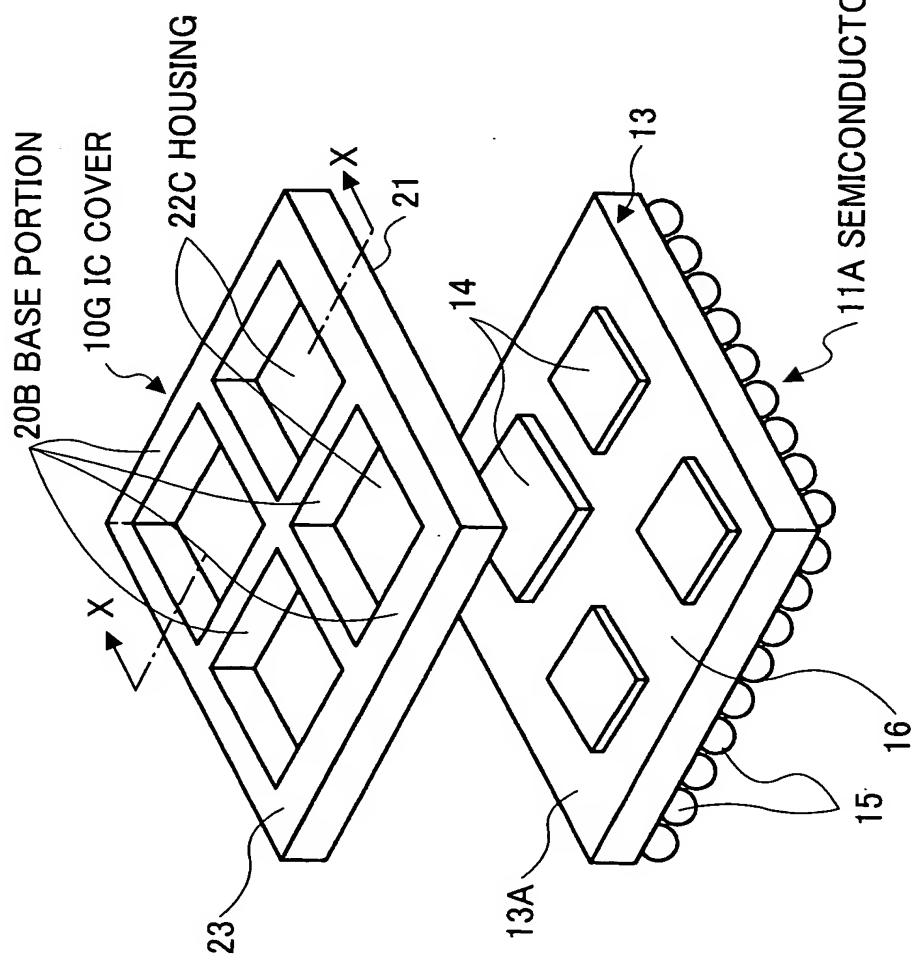


FIG. 14B

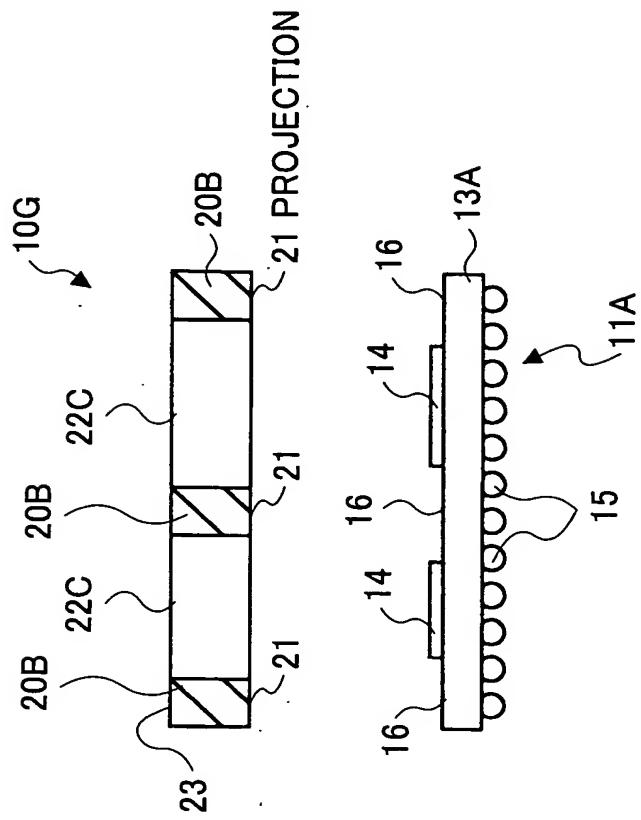


FIG. 15A

12B
SEMICONDUCTOR DEVICE UNIT

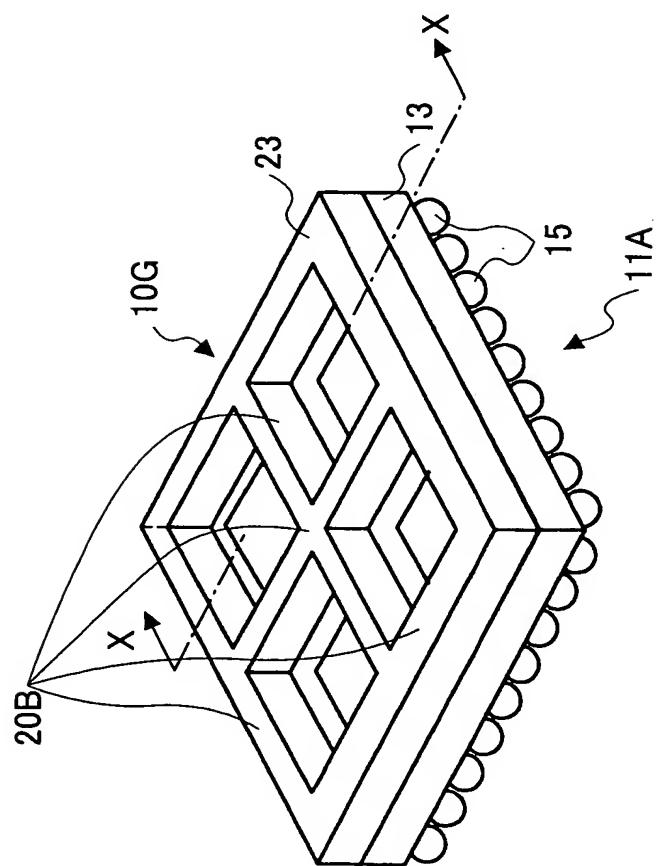


FIG. 15B

12B

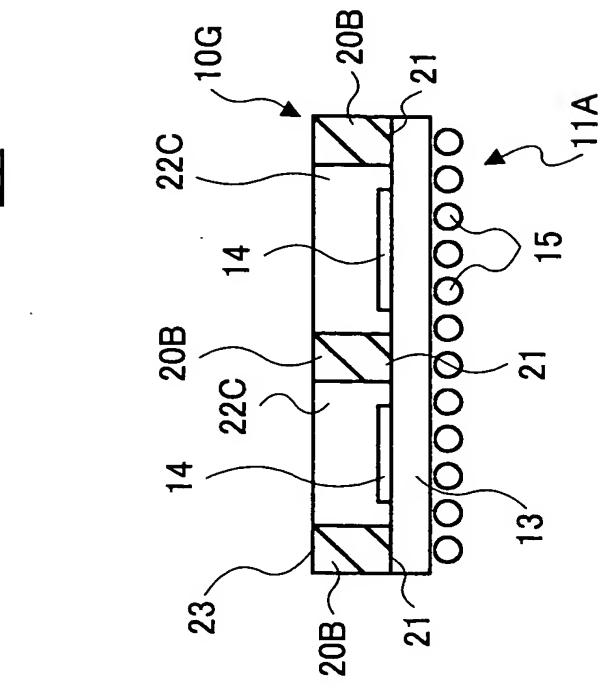


FIG. 16A
FIG. 16B

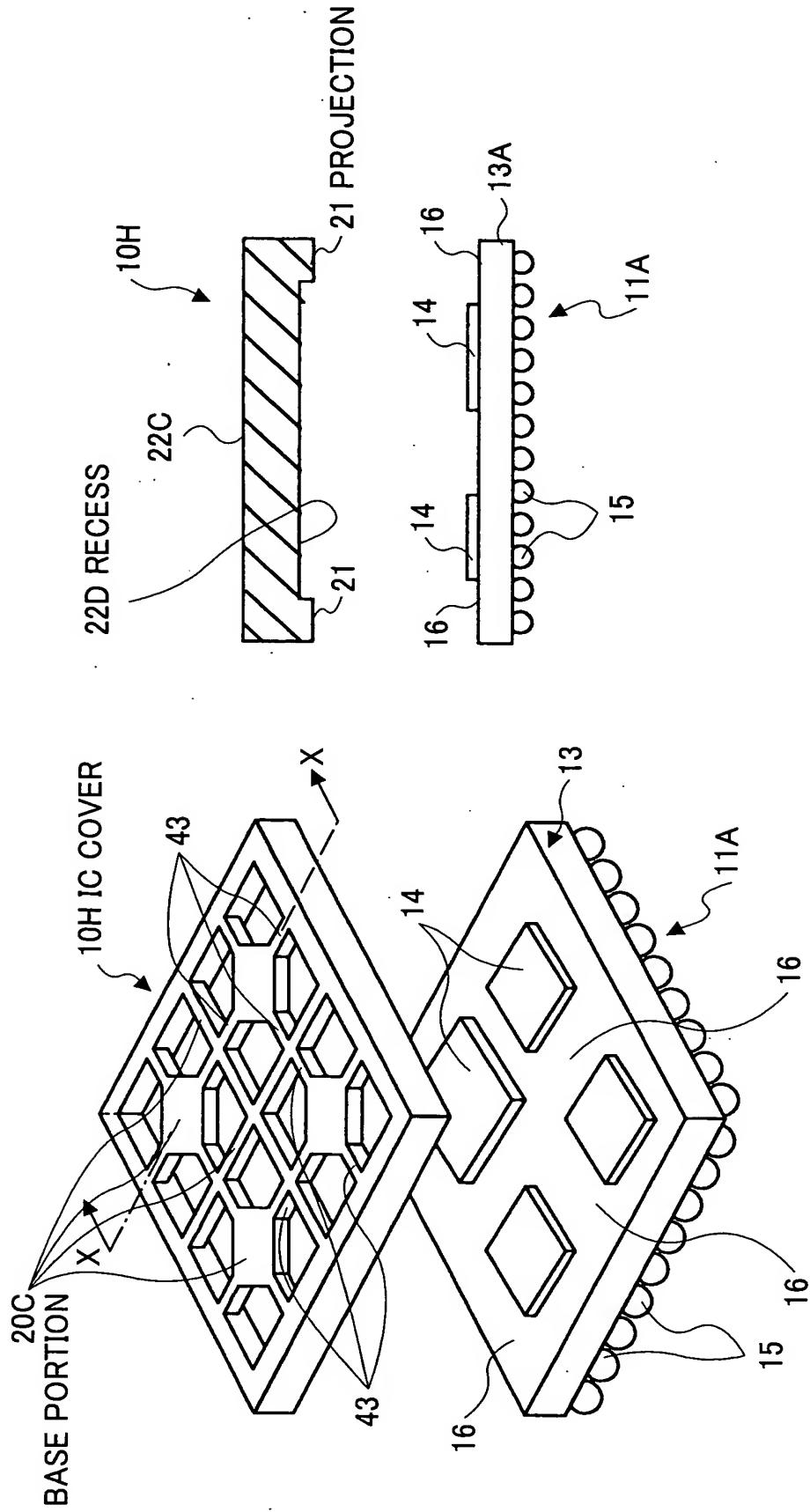


FIG. 17A
FIG. 17B

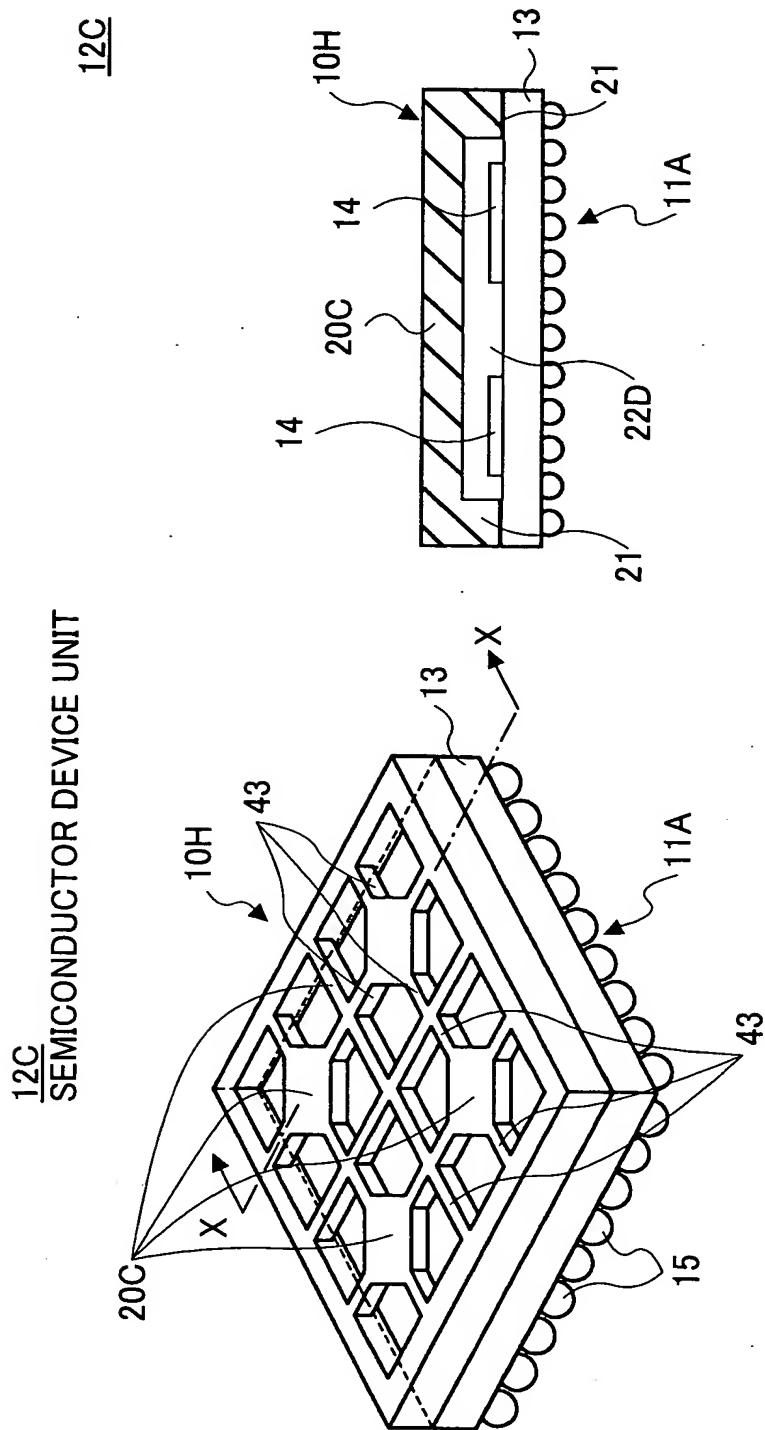


FIG.18A

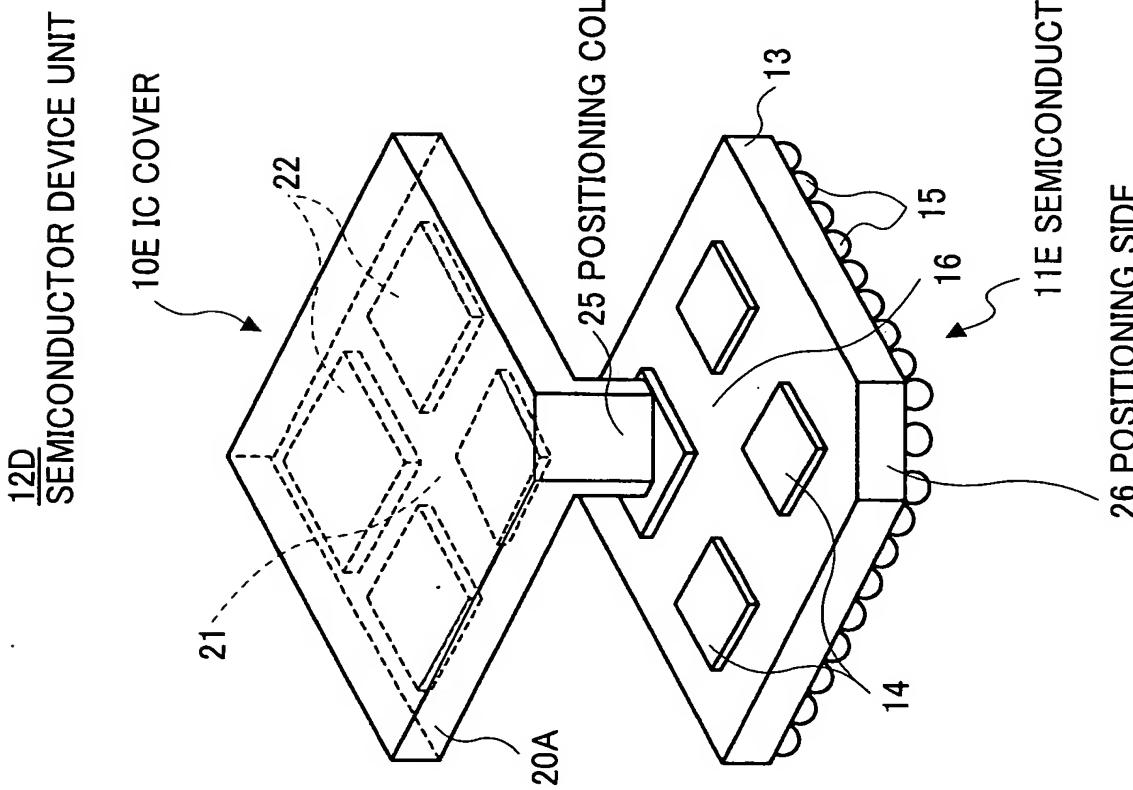


FIG.18B

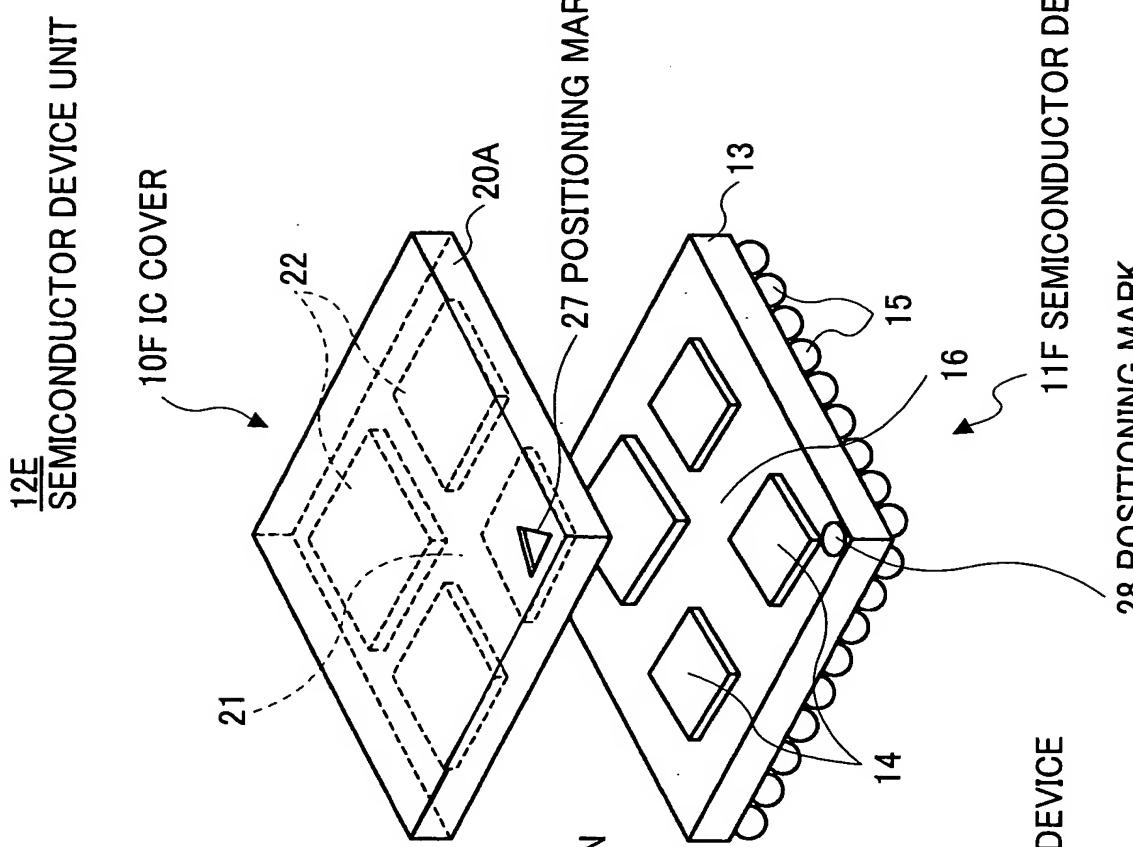


FIG.19

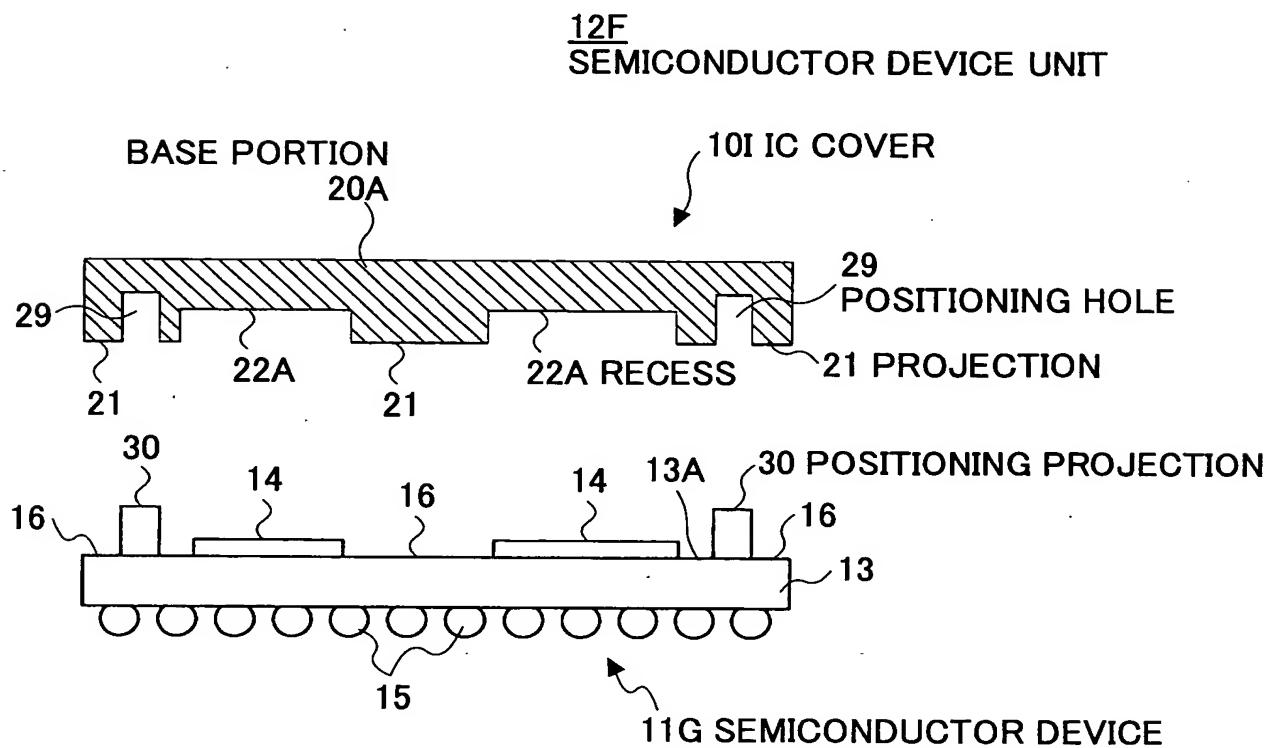


FIG.20

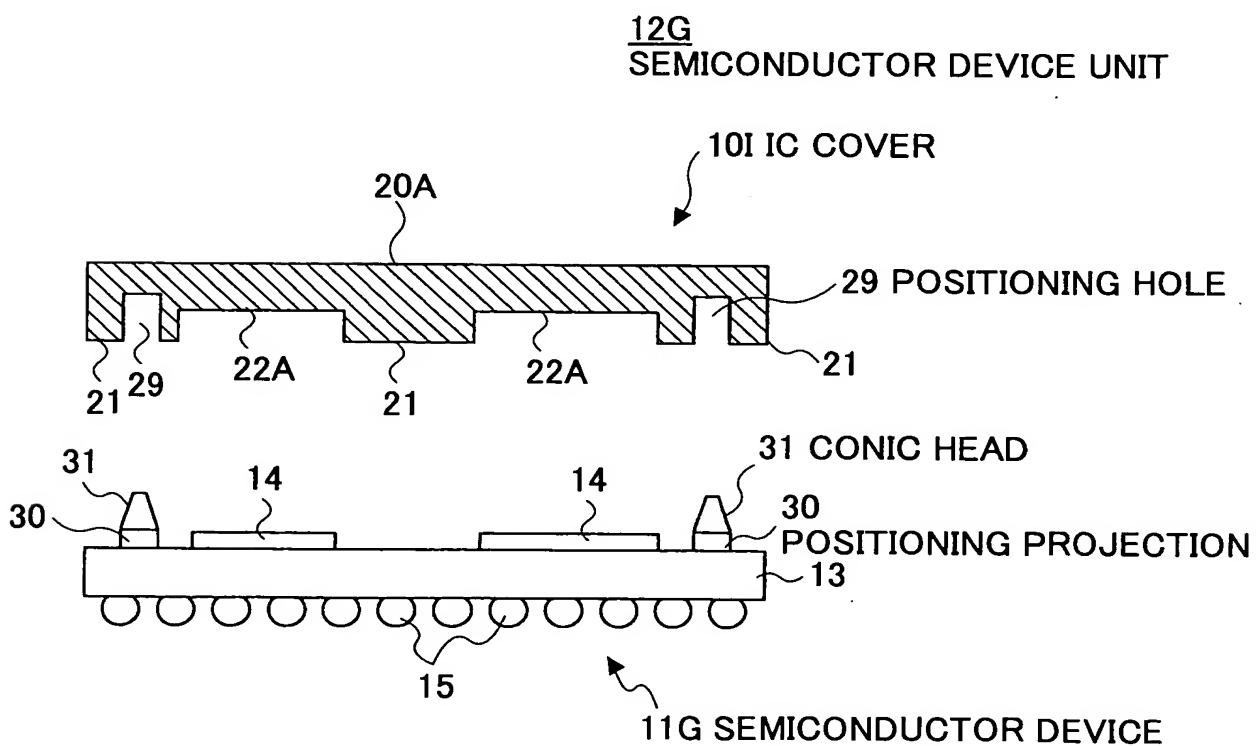


FIG.21

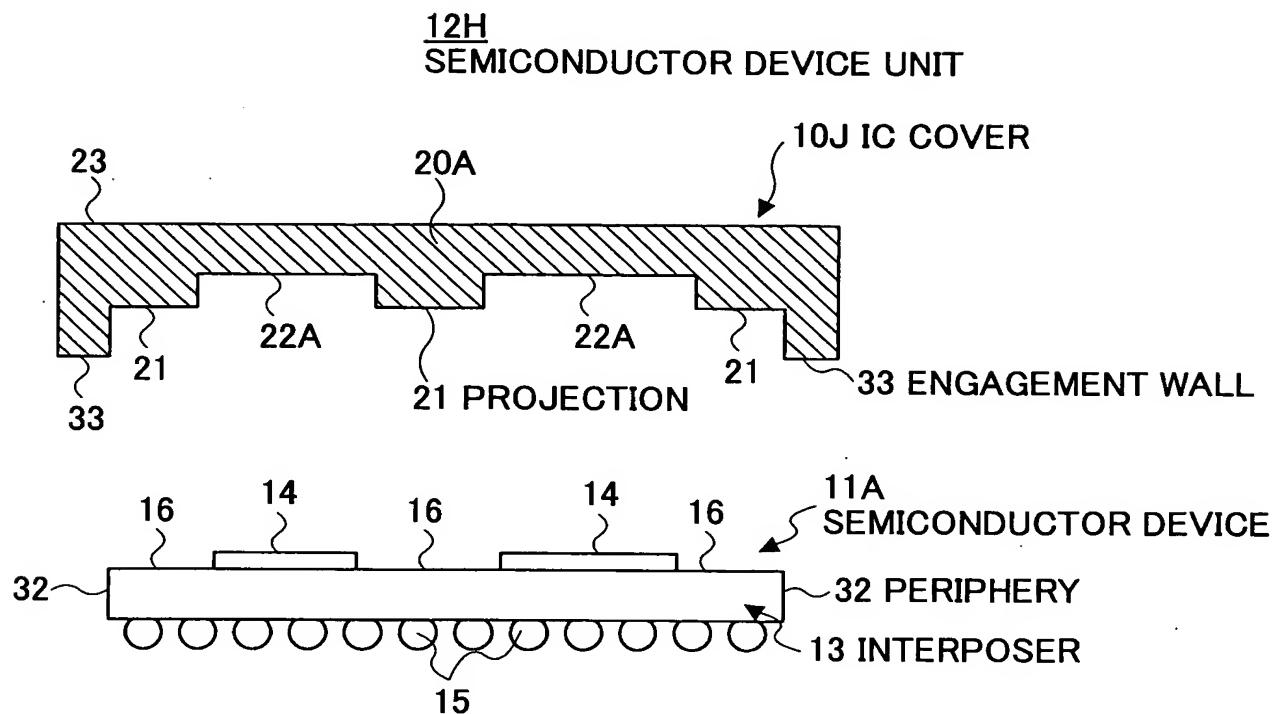


FIG.22

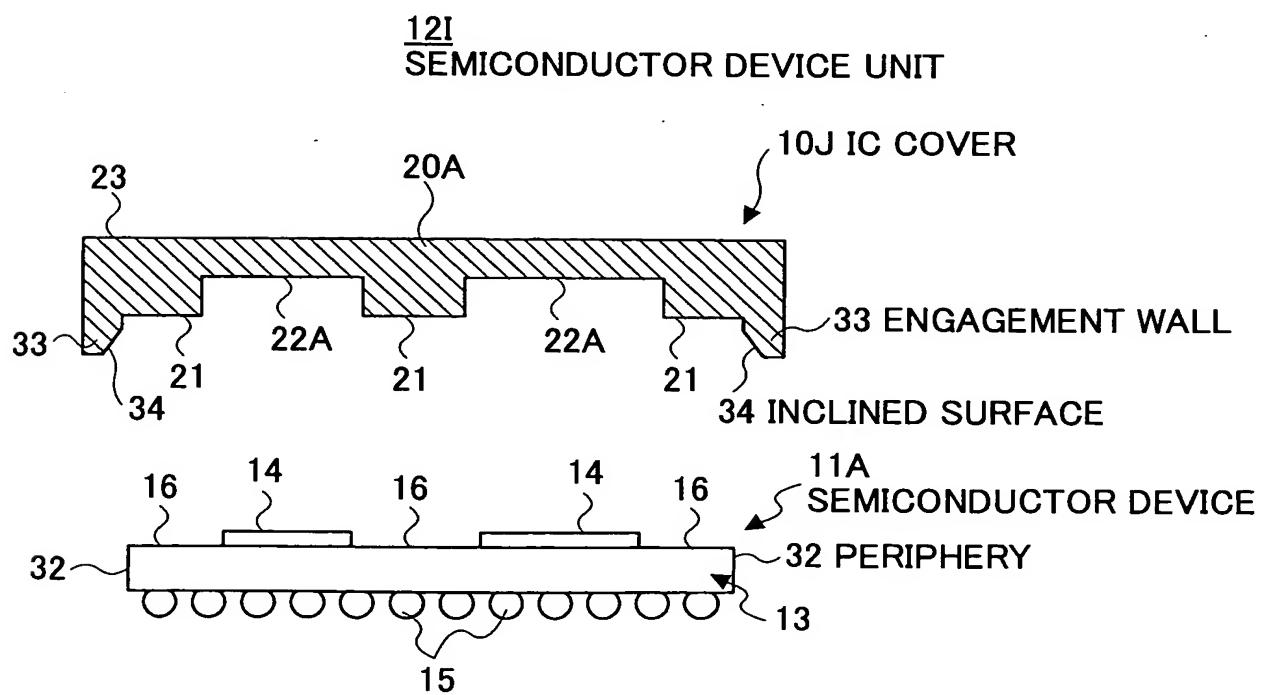


FIG.23

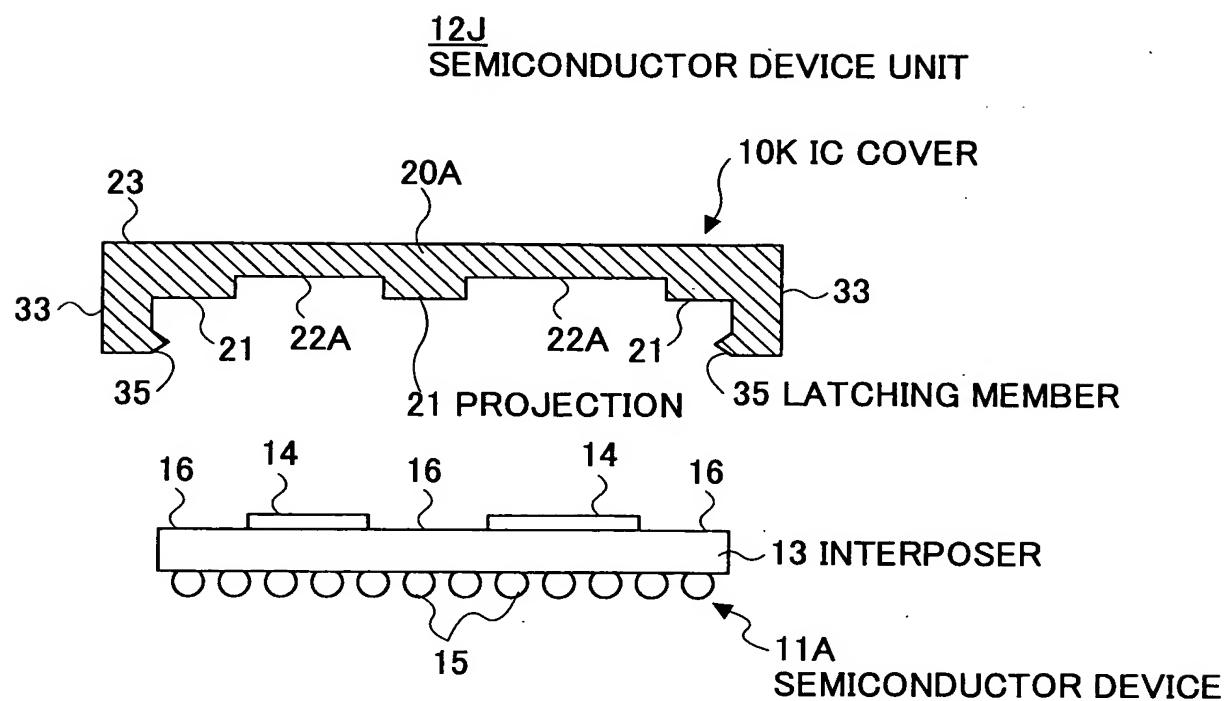


FIG.24A

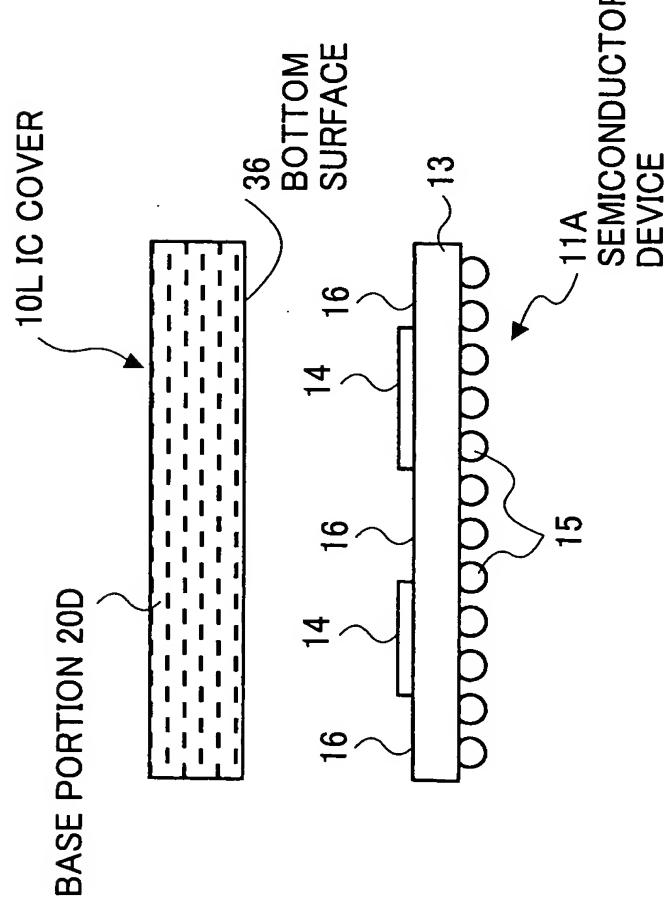


FIG.24B

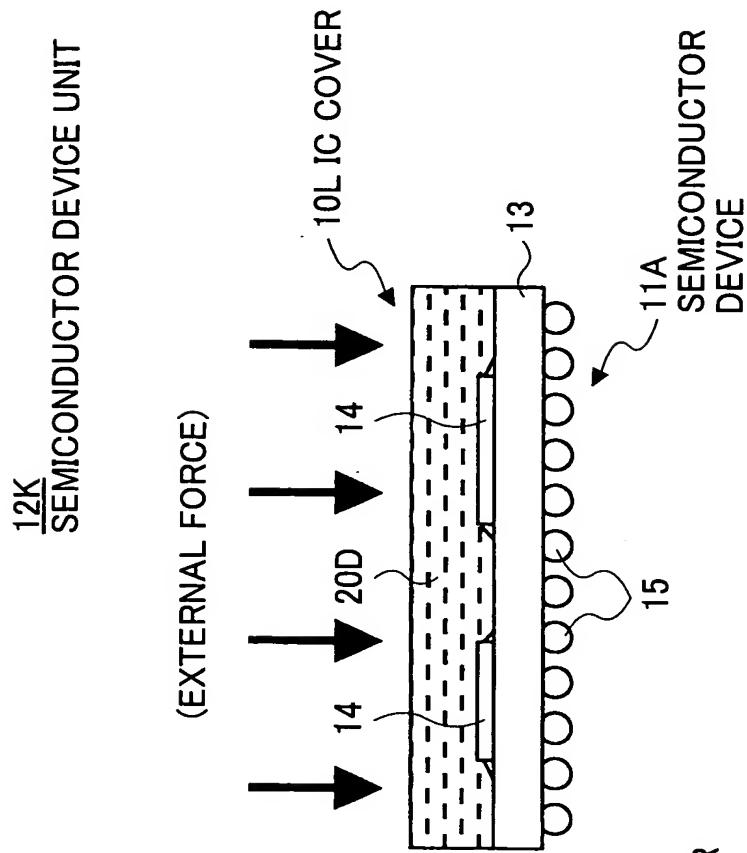


FIG.25A

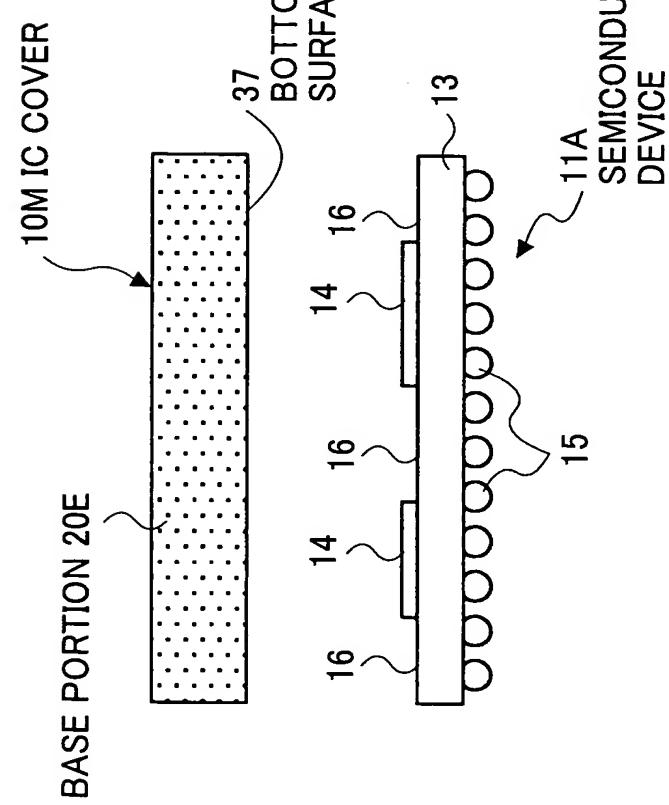


FIG.25B

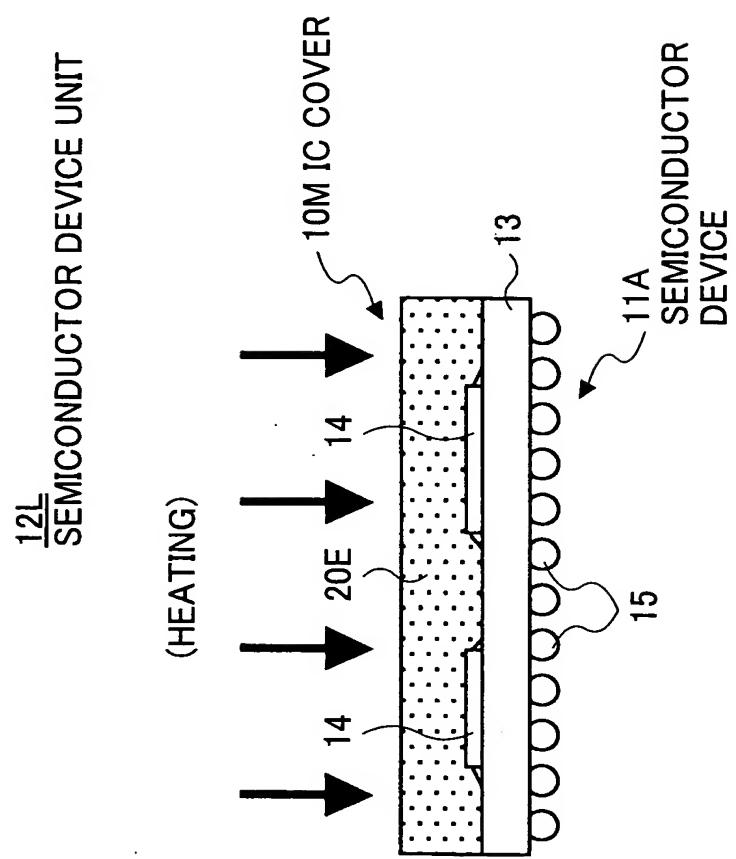


FIG.26

